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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Brian D. Trotter et al.  
Assignee: Cirrus Logic, Inc.  
Title: MULTIBIT DELTA-SIGMA MODULATOR WITH VARIABLE-LEVEL  
QUANTIZER  
Serial No.: 10/699,466 Filing Date: October 31, 2003  
Examiner: Group Art Unit: 2817  
Docket No.: 1451-CA

Austin, Texas  
May 12, 2004

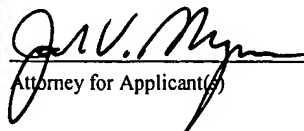
COMMISSIONER FOR PATENTS  
Mail Stop PGPUB Drawings  
P.O. Box 1450  
Arlington, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

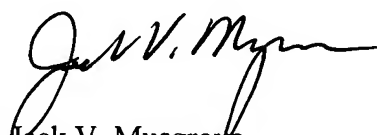
Applicant submits herewith four (4) sheets of formal drawings, consisting of Figures 1-7, in the above-identified application. Applicant respectfully requests that the Examiner approve these drawings and forward the same to the Official Draftsperson.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Mail Stop PGPUB Drawings, P.O. Box 1450, Arlington, VA 22313-1450, on May 12, 2004.

  
Attorney for Applicant(s)

May 12, 2004  
Date of Signature

Respectfully submitted,

  
Jack V. Musgrove  
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Reg. No. 31,986